



Form PTO 1449 (Rev. 2-32)				U.S. Department of Commerce Patent and Trademark Office		Atty. Docket No. SUN-P5447		Serial No. 09/823,207	
Information Disclosure Statement by Applicant						Applicant: Partha P. Tirumalai et al.			
(Use several sheets if necessary)						Filed: March 30, 2001 Group: 2122			
<b>U.S. Patent Documents</b>									
Init.		Document No.	Date	Name	Class	Subclass	Filing Date		
252	1	5,493,675	02/20/1996	Faiman, Jr. et al.	395	700	05/26/1994		
252	2	6,078,744	06/20/2000	Wolezko et al.	395	705	08/01/1997		
252	3	6,289,505 B1	09/11/2001	Goebel	717	9	11/18/1997		
252	4	6,292,938 B1	09/18/2001	Sarkar et al.	717	6	12/02/1998		
252	5	6,295,643 B1	09/25/2001	Brown et al.	717	9	12/10/1998		
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<b>Foreign Documents</b>									
<b>Technology Center 2100</b>									
Translation									
Init.		Document No.	Date	Country	Class	Subclass	Yes	No	
<b>Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)</b>									
252	6	T. Williams, "Compiler Technology Tracks CPU Architecture Headway", <u>Embedded Systems Development</u> , pp. 41-45, August 2000.							
Examiner:					Date Considered 6/4/04				
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.									